

# VINODAPATIL

6360611088 | [vinodapatil33@gmail.com](mailto:vinodapatil33@gmail.com) | <https://www.linkedin.com/in/vinoda-patil-b018b1255/>

---

Enthusiastic and detail-oriented educator with a strong academic background and hands-on industry experience in embedded systems. Currently working as an Assistant Professor, delivering lectures, mentoring students, and guiding projects in electrical and electronics engineering. Previously worked in the R&D domain as a Trainee Embedded Developer, which adds practical insights to classroom teaching. Passionate about integrating real-world applications into academics, fostering student engagement, and continuously enhancing the learning environment through innovative teaching methodologies.

## WORK EXPERIENCE

---

- BGMIT College Mudhol – Assistant Professor. Sept 2024–Present.
- Phase Power Protection PVT LTD, Bangalore-Research and Development. Nov 2023–Sept 2024.
- Developed and tested embedded systems software for OMAP-L138C6000 DSP+ARM Processor using C.
- Implemented **over current** protection functions, including **derived neutral over current** and **Under phase current protection**.
- Created and optimized DSP algorithms for real-time signal processing applications. Proficient in DSP processor concepts, algorithms, and development tools such as CCS5.

## INTERNSHIP

---

- KPCL Ambika Nagar Dandeli. June 11 to 24 2019
- Murudeshwar Power Corporation Narayanapur. Jan 3 to 31 2019.
- Power Grid Corporation of India Limited, Narendra 400/220kv Substation, Dharwad. Sep 28 to Oct 16 2021

## PROFESSIONAL TRAINING

---

- **Vector India, Bengaluru Embedded Systems.** Dec 2022-June 2023
- Programming Languages: C, C++, Embedded C.
- Operating Systems: Linux, Windows.
- Protocol: I2C, UART, SPI, CAN.

## INTERPERSONAL SKILLS

---

- Active listening    • Time Management    • Quick Learner    • Leadership    • Motivation

## TOOLS

---

- **Development environment:** Code composer studio 5, Code composer studio 7, Visual studio & Eclipse IDE & Keil
- **Debugging tools:** stm32 cube programmer

- WINS CP
- Putty
- Proteus
- U buntu
- Matlab/Simulink
- Power word simulator.

## EDUCATION

---

### M. Tech in Power System Engineering SDMCET Dharwad

2020- 2022

- CGPA: 9.06.

#### Technical Seminar:

- Simulation of Grid Connected EV Charging. Station with Renewable Energy Source.
- Improvement of Power Quality Using Unified Power Flow Controller.

#### Project:

- Simulation & Analysis of a Three-Phase Shunt Active Power Filter.(Mini Project).
- Mitigation of Power System Transients In A Multi-Machine System Using UPFC. (Major Project)

### BE in Electrical & Electronics BLDEACET Vijayapur

2016-2020

- CGPA:6.6

#### Paper Publication:

- **Optimization of Power System Transient in a Multi-Machine System Using Unified Power Flow Controller** Second International Conference on Electronics and Renewable System (ICEARS 2023)
- **Congestion Management by Phase Shifting Transformer Using Fuzzy Logic Control.**2023 Global Conference on Information Technologies and Communications (GCITC)
- **Mitigation of Power System Transients in a Multi Machine System Using STATCOM.** International Journal for Research in Applied Science & Engineering Technology.
- **Simulation and Analysis of a Three- Phase Shunt Active Power Filter** International Journal of Scientific Research in Engineering and Management.

## COMMUNICATION SKILL

---

- English
- Kannada

## DECLARATION

---

I do here by declare that the above in formation is true to the best of my knowledge.

**Date:**

**Place:**

(vinoda patil)