

BIO DATA

NAME: Varun P Sarvade
DESIGNATION: Assistant Professor
DEPARTMENT: Computer Science Engineering
BGMIT. MUDHOL– 587 313, Karnataka, INDIA
DATE OF BIRTH: 24/07/1987
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GENDER: Male

EDUCATION DETAILS:

Qualification	Specialization	Institute	Year of Award
M.Tech	Computer Science & Engineering	BIET, Davangere	2014
B.E.	Instrumentation Technology	BEC, Bagalkot	2009

Title of M.Tech Thesis: Off-Line Signature Verification System using Fuzzy Logic & Neural Network Approaches.

Title of B.E. Project: Unmanned Railway Gate Control: Using Wireless Technology

AREAS OF INTEREST/RESEARCH: Digital Image Processing, Microprocessors, Computer Networks, Digital Electronics.

ACADEMICS:

Sl.No	Subjects Taught
1	Analog & Digital Electronics
2	Advanced Microprocessors
3	Computer Organization
4	Cryptography, Network Security & Cyber Laws
5	Management & Entrepreneurship
6	C-Programming
7	Embedded Computing Systems

EXPERIENCE: Teaching: 07 Years, Industry: 01 Year

PUBLICATIONS:

International Journals:

1. “A Survey On Automatic Signature Verification Techniques”, Varun Sarvade, Supriya Sarvade, International Journal of Advanced Computing and Electronics Technology, Sept. 2015.

International Conferences:

1. “Performance analysis of IEEE 802.11 ac for vehicular networks using realistic traffic scenarios”, Varun P Sarvade, Dr.S.A Kulkarni, Advances in Computing, Communications and Informatics (ICACCI) IEEE Conference, Sept.2017.

ADMINISTRATIVE RESPONSIBILITIES:

Sl. No.	Nature of Work (Chairman, Coordinator, member etc.)	Worked for (University, College, etc.)	Period
1	AICTE Member	College	2014-15
2	AICTE Coordinator	College	2015-17
3	VTU-LIC Coordinator	College	2015-16
4	FOSS Coordinator	College	2017- till date
5	CSE Students Association Coordinator	Department	2017-18

WORKSHOPS ATTENDED:

Sl. No	Name of Workshop	Venue	Date (No. of Days)
1	Data Science and Classifications practicing with Python and Matlab	JNNCE , Shivamogga	16th July to 21st July 2018
2	RESEARCH METHODOLOGY	VTU Centre for Post Graduate Studies Bengaluru Region, Muddenahalli	26th FEBRUARY - 03rd MARCH 2018
3	Advanced workshop on NS3 Simulator	BITM, Bellary	26 th January to 28 th January 2017
4	Embedded System Design	BVB, Hubli	13 th January to 28 th January 2014
5	Feel Teacher	BIET, Davangere	19 th April to 21 st April 2013
6	Low Power Embedded Systems using Texas Instruments MSP430	BIET, Davangere	9 th February to 10 th February 2012