

## RESUME

Mr. VinayShettar  
Assistant Professor  
Dept. of ECE  
BGMIT, Mudhol  
Mudhol-587313  
Dist: Bagalkot  
Karnataka, India.



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### OBJECTIVE:

I wish to work in an organization, which appreciates my efforts and provide guidance with a healthy environment to bring out the best of my ability and contribute towards the growth of organization.

### EDUCATION:

Course	Institution	University / Board	Year of Passing	Percentage/ Class Secured
M.Tech (Digital Communication)	Shri B.V.V Sangha's Basaveshwar Engineering College, Bagalkot.	Visvesvaraya Technological University, Belgaum	2014	9.64 (CGPA) ( <i>GOLD MEDAL</i> )
B.E (ECE)	Smt. Kamala and Shri. Venkappa M Agadi College Engineering and Technology Laxmeshwar.	Visvesvaraya Technological University, Belgaum	2012	74.5%
PUC/12 <sup>th</sup>	S M Bhoomaraddi PU College Gajendragad	Dept of P.U.Education, Karnataka Board	2008	69%

S.S.L.C/10 <sup>th</sup>	Gurumahanteshwar High School Sudi	Karnataka Secondary Education Board.	2006	87.68%
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#### TECHNICAL PROFICIENCY:

**Subjects Handled:** Basic Electronics, Principle of Communication Systems, Field Theory, Linear Integrated Circuits, Microwave and Radar, Digital Communication, Micro and Smart Systems, Multimedia Communications.

**Labs Handled:** Analog Electronics circuits, Linear Integrated Circuits, Digital Signal Processing, Digital Communication lab.

**Tools:** Comsol Multi physics, Coventor ware and Matlab.

#### PERSONAL SKILLS:

Punctuality, Hard working,

Quick learner,

Team worker and

Good Time management skills.

#### ACHIVEMENTS

- ❖ Secured **GOLD MEDAL** in M.Tech with the CGPA of 9.64

#### EXPERIENCE

- Handled Electronic Devices and Circuits lab, Advanced communication lab, Industrial Electronics lab and Signals and Systems lab for UG students during 2012-13 and 2013-14 as **TEQIP Assisted Student**.
- Since from August-2014 Working as **Assistant Professor** in Electronics and Communication Engineering Department of Biluru Gurubasav Mahaswamiji Institute of Technology Mudhol ( **4 Years of Teaching Experience** ).

#### ACADEMIC PROJECTS:

**BE Project Title: Implementation of BIST Capability using LFSR Techniques in UART**

**Description:** The increasing growth of sub-micron technology has resulted in the difficulty of VLSI testing. Test and design for testability are recognized today as critical to a successful design. Built-in-Self- Test (BIST) is becoming an alternative solution to the rising costs of external electrical testing and increasing complexity of devices. Small increase in the cost of system reduces large testing cost. BIST is a design technique that allows a circuit to test itself. Test pattern generator (TPG) using Linear Feedback Shift Register (LFSR) is proposed which

is more suitable for BIST architecture. We have implemented Universal asynchronous receiver transmitter (UART) with BIST capability using different LFSR techniques.

### **M.TechProject Title: DESIGN AND SIMULATION OF MEMS PRESSURE SENSOR**

**Description:** The world is getting digitalized, demands for new and emerging technologies have reached its peak, and customer demands have taken a U-turn. To cope with such unique requirements many systems and system devices are into the market and one of such enhancing technology is MEMS. MEMS are systems of small size, light weight, enhanced performance and reliability finding widest of applications in sectors of Automotive and instrumentation. A typical MEMS Sensor is at least one order of magnitude smaller than the traditional sensors used to measure instantaneous flow quantities like Pressure and Velocity. In this we focus on the outlook of measurement of Pressures and provide general background Design Criteria. These micro pressure sensors can also resolve all relevant scales even in high Reynolds number turbulent flows and arrays of micro pressure sensors make it feasible.

### **PUBLICATIONS:**

#### **Technical papers published in journals:**

1. **VinayShettar**, Sneha B. Kotin, Kirankumar B. B, B. G. Sheeparamatti “SIMULATION OF DIFFERENT MEMS PRESSURE SENSORS” International J. of Multidispl. Research & Advcs. in Engg.(IJMRAE),ISSN 0975-7074, Vol. 6, No. II (April 2014), pp. 73-81 (International Conference on Power Electronics and Drives-2014)
2. Kirankumar B. B, **VinayShettar**, Sneha B. Kotin, B. G. Sheeparamatti “DESIGN AND SIMULATION OF MEMS CAPACITIVE PRESSURE SENSOR” Second International Conference on Emerging Research in Computing, Information, Communication and Applications’(ERCICA-14)
3. BageerathiG,Chetan M, Tameez J, **VinayShettar**, Ravi Bashetti, SnehaKotin “Design and simulation for different geometries of MEMS cantilever” National Conference on recent technologies in Electrical and Electronics: ISBN-978-81-927555-9-5
4. Saraswati U, AshwiniC,BasavarajB,Ashwini P, Ravi Bashetti, SnehaKotin, **VinayShettar**“Design and simulation of piezo electric MEMS based mender shaped energy harvester” National Conference on recent technologies in Electrical and Electronics: ISBN-978-81-927555-9-5
5. SavitaS,HeenaG,AshokM,SnehaKotin, **VinayShettar**,Ravi Bashetti“DESIGN AND SIMULATION OF CAPACITIVE PRESSURE SENSOR” National Conference on recent technologies in Electrical and Electronics: ISBN-978-81-927555-9-5

6. VarshaTadas, ShrutiPatil, VinayakBadiger, Santosh Banakar, **VinayShettar**, Ravi Bashetti, SnehaKotin “ A CASE STUDY ON MEMS BASED PIEZORESISTIVE PRESSURE SENSOR ” International Journal of Current Engineering and Scientific Research (IJCESR):ISSN:2394 0697 DOI:10.21276/ijcesr
7. Suma H, Soujanya B, FirozShekh, Savita G, Ravi Bashetti, SnehaKotin, **VinayShettar** “DESIGN AND SIMULATION OF MEMS BASED CAPACITIVE ACCELEROMETER” International Journal of Current Engineering and Scientific Research (IJCESR):ISSN:2394 0697 DOI:10.21276/ijcesr

#### **WORKSHOP ATTENDED:**

- Attended Workshop on SWARM ROBOTICS at NITK SURATHKAL.
- Given power point presentation on **Brain Machine Interfacing** at SKSVMACET Laxmeshwar
- Two days Student Development Programme on “**Rapid Qualnet Simulator**”, 14th-15<sup>th</sup> May 2013 at Basaveshwar Engineering College, Bagalkot.
- Three days Faculty Development Programme on “**Recent Advances in Wireless Networks**” from 16<sup>th</sup> to 18<sup>th</sup> May 2013 at Basaveshwar Engineering College, Bagalkot.
- Three days National Level Workshop on “**Implementation of Signal Processing Algorithms Using FPGAs**” from 23<sup>th</sup> to 25<sup>th</sup> May 2013 at Basaveshwar Engineering College, Bagalkot.
- Three days National Level Workshop on “**Technical Documentation Using LaTeX**” from 27<sup>th</sup> to 29<sup>th</sup> May 2013 at Basaveshwar Engineering College, Bagalkot.
- Three days Faculty Development Programme on “**Soft and Evolutionary Computing Techniques and its Applications**” from 30<sup>th</sup> May to 1<sup>st</sup> June 2013 at Basaveshwar Engineering College, Bagalkot.
- One day workshop on “**COMSOL-Multiphysics**” on 25<sup>th</sup> April 2014 at Kshema-in Belagum
- Three days National level workshop on “**Intel Atom Processor based Embedded system Design and Development**” from 28<sup>th</sup> to 30<sup>th</sup> May 2014 at Basaveshwar Engineering College, Bagalkot, Karnataka.
- Three days Workshop on “**Embedded and IoT Training Using Arduino Platform**” from 02<sup>nd</sup> to 04<sup>th</sup> February 2018 at BGMIT, Mudhol, Karnataka.

- A Short Term Course on “ **MEMS- Based Sensors: Fabrication and Characterization Techniques**” from 12<sup>nd</sup> to 16<sup>th</sup> February, 2018 at Indian Institute of Science Bengaluru.

#### EVENT ORGANIZED

- Three days Workshop on “**Embedded and IoT Training Using Arduino Platform**” from 02<sup>nd</sup> to 04<sup>th</sup> February 2018 at BGMIT, Mudhol, Karnataka.
- Given Talk on “**MEMS Technology**” at S R Vastrad Polytechnique Guledagudda.

#### PROFESSIONAL ACTIVITIES

- Student President for E-SPECTRA for the year 2011-12 in E & C Engg SKSVMACET Laxmeshwar
- Since from August-2014 Working as *Chairman Gymkhana* and Successfully hosted Men’s VTU *Kalaburagi zonal KHO-KHO* and *Kabaddi* Tournaments @ BGMIT, Mudhol.

#### PERSONAL PROFILE:

Name : VinayShettar  
Father’s Name : Shivappa  
Date of Birth : 4<sup>th</sup> Aug, 1990  
Gender : Male  
Marital Status : Married  
Nationality : Indian  
Languages Known : Kannada, English  
Hobbies : Listening Music, Playing chess and Volley ball  
Permanent Address : Mr. VinayShettar  
Assistant Professor  
Dept. of ECE  
BGMIT, Mudhol  
Mudhol-587313  
Dist: Bagalkot  
Karnataka, India.

#### DECLARATION:

I Mr.Vinay Shettar hereby declare that the details given above are correct and true to the best of my knowledge and belief.

Place: Mudhol

Date: 03/07/2018

(VINAY SHETTAR)