

ABHISHEK K HADAGLIMATH

Address:

DR. Kumarswamy.V.H
Plot num 119,jayanagar,doctors colony,
Gulbarga -585105

✉ : abhishek.kh88@gmail.com

☎ : +91-9591770692

Objective:

To work in an environment that provides a challenging and rewarding career ensuring a high level job satisfaction. In the process apart from benefiting my employer, I also expect to learn for my overall development.

Educational Qualification:

Degree	Institution	University	% Obtained	Year of passing
M.Tech (Digital communication)	BEC, Bagalkot	VTU	81.70	Aug 2013
B.E (Electronics & Communication Engineering)	PDACE, Gulbarga	VTU	58.50	July 2010
PUC	CPEMS, Gulbarga	Karnataka PU Board	67.41	June 2006
10th	CPEMS, Gulbarga	CBSE	70.10	May 2004

Experience Details:

➤ Wipro.	4 months (Feb to may 2011)	Trainee
➤ AIET College, Gulbarga.	1 Semester (Feb 2013 to May 2013)	Lecturer
➤ BGMIT,Mudhol	From Aug 2014 till date	Asst Prof

Technical skills:

- Logic Design, Digital communication.
- Speech Processing.
- C, Core Java, VLSI\VHDL,MATLAB.

Project undertaken:

Title(M.Tech) Implementation of Speaker recognition algorithm using FPGA

Description Speaker recognition is the process of automatically recognizing the speaker voice according to the basis of individual information in the voice waves. It is a branch of biometric authentication where it is one of the fast gaining popularity as means of security measures due to its unique physical characteristics and identification of individuals. The research on speech recognition technology mainly concentrates on two aspects. One is the software running on computer, the other is embedded systems. The advantages of Embedded systems are high-performance, and convenience, cheap and they have huge potential for development.

Title(B.E) Wireless DC Motor Control

Description This project aims at regulating speed of DC Motor used in many industrial uses, by use of radio frequency. This title of our project is High-performance low-cost, low-loss, wireless DC motor speed control unit. A speed control with both low-cost and good performance will be highly marketable, especially for small mobility applications.

Activities:

- Member, organizing committee for conducting workshops on MATLAB, VLSI\VHDL at BEC Bagalkot.
- Volunteer for better India, an initiative of Art of living.
- Kumudvathi river rejuvenation project

Personal information:

Father Name	Dr.Kumarswamy.V.H
Mother Name	Mrs.Anuradha kumarswamy
Marital Status	Single
Date of Birth	23 Feb 1988
Languages Known	English, Kannada and Hindi.
Hobbies	Listening to music, Playing Cricket, Yoga.

Declaration:

I hereby declare that the information furnished above is true to the best of my knowledge.

Place: Gulbarga

Date: 31-03-2015

Abhishek K.H